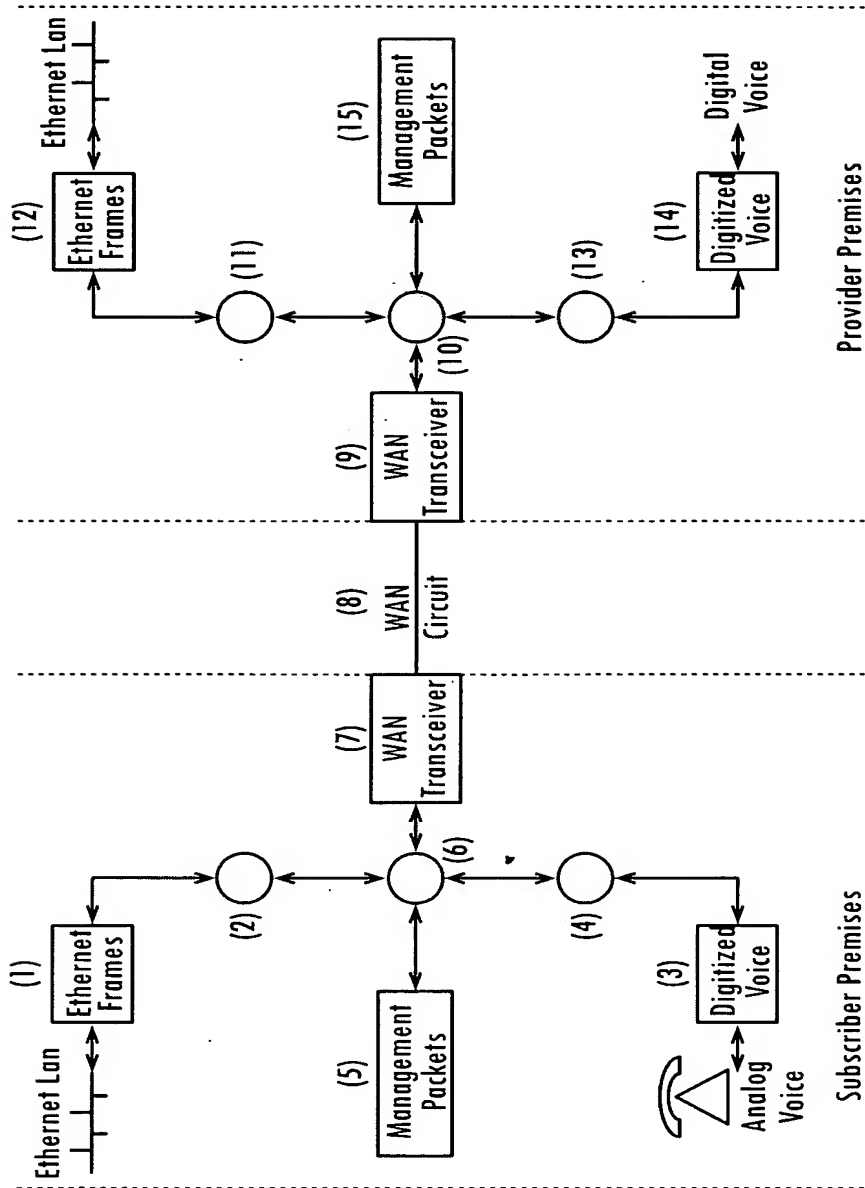




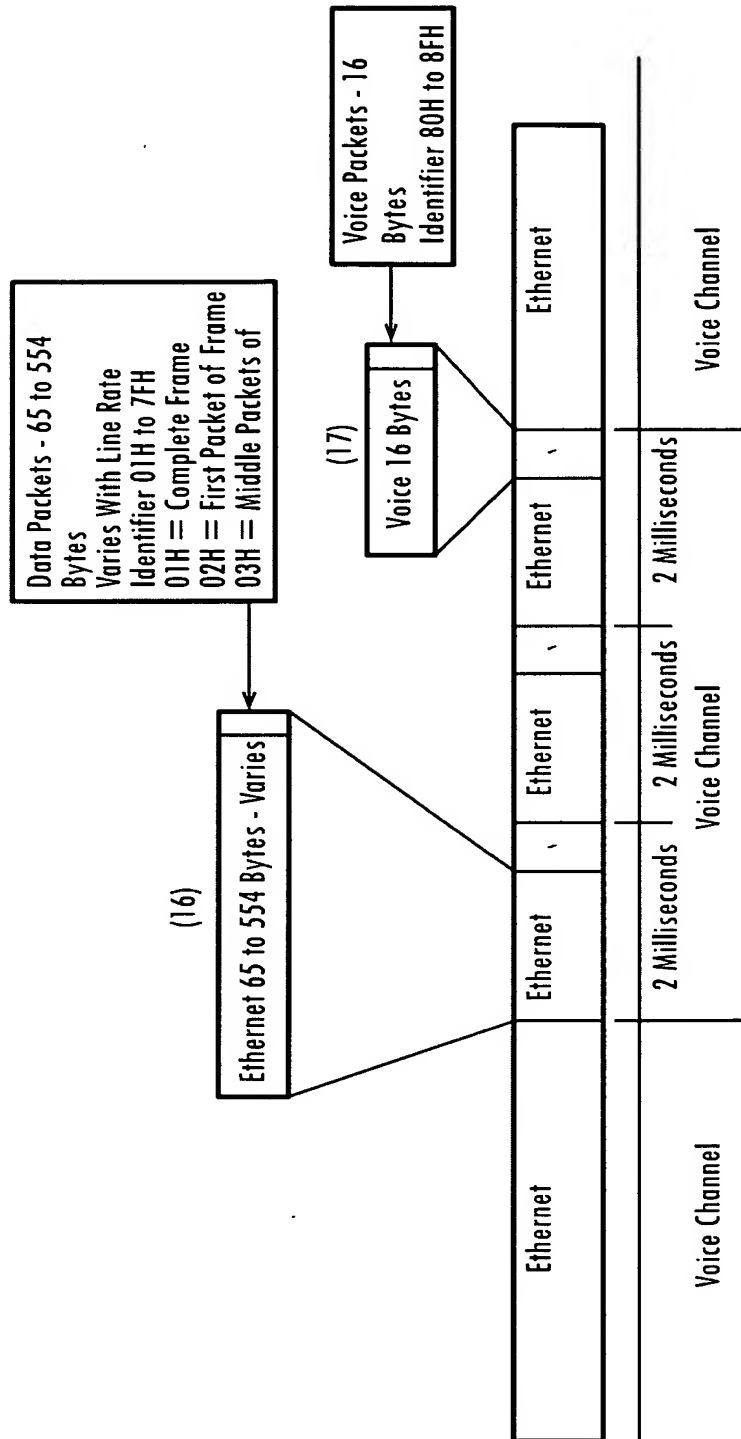
1/8



Data Flow Through the System

**Fig. 1**

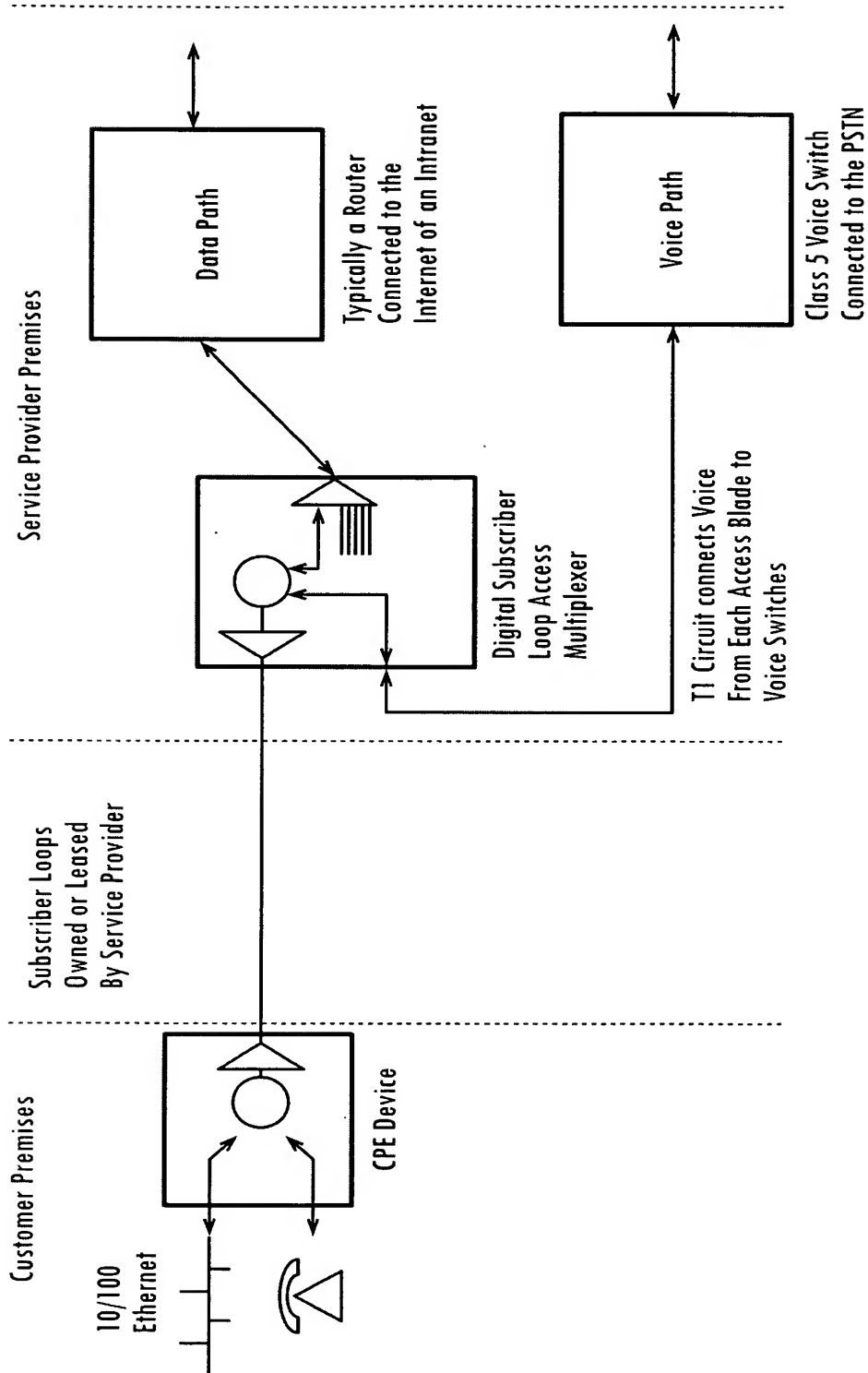
2/8



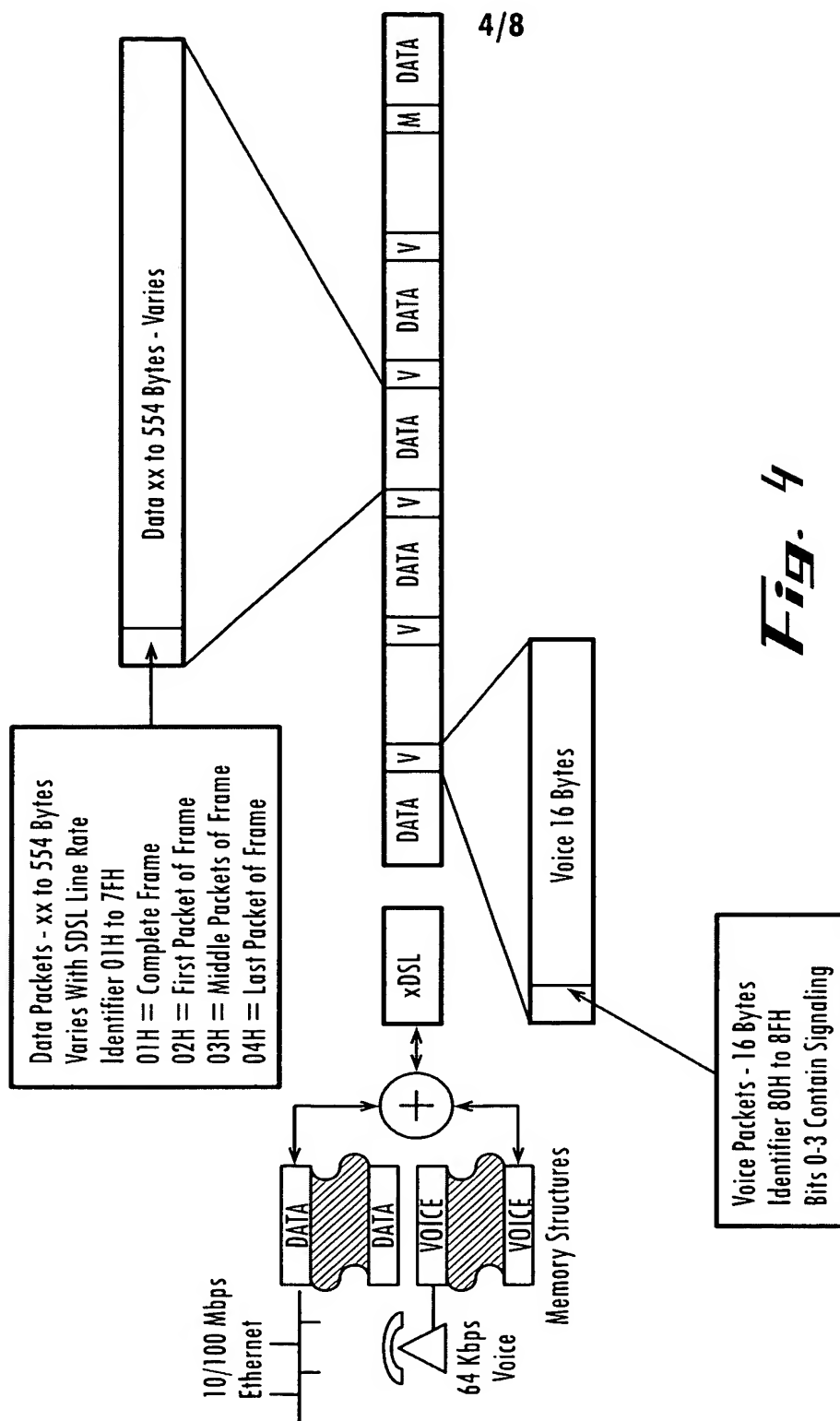
Packet Structure on the WAN Circuit

**Fig. 2**

3/8

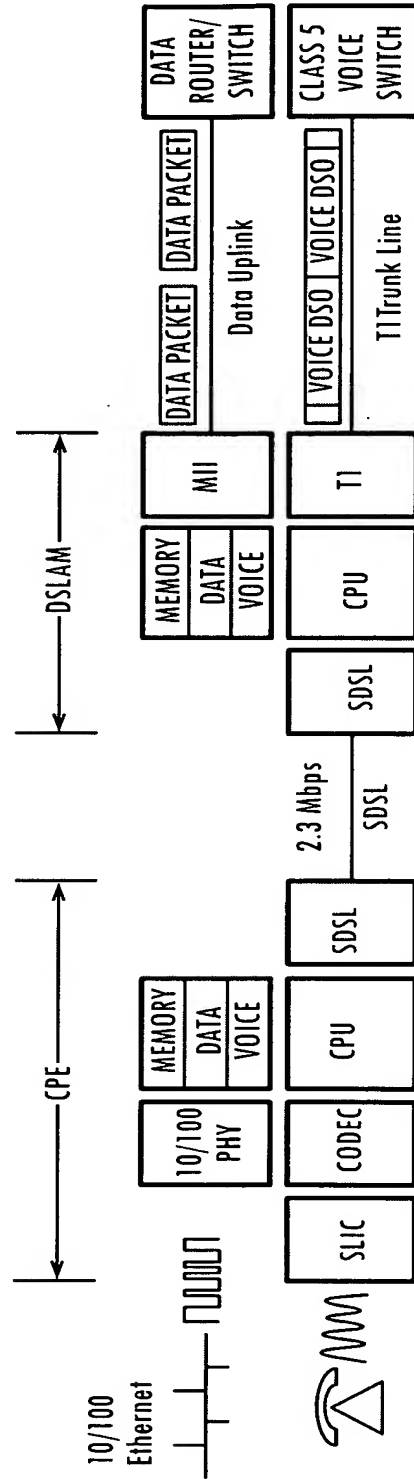


**Fig. 3**

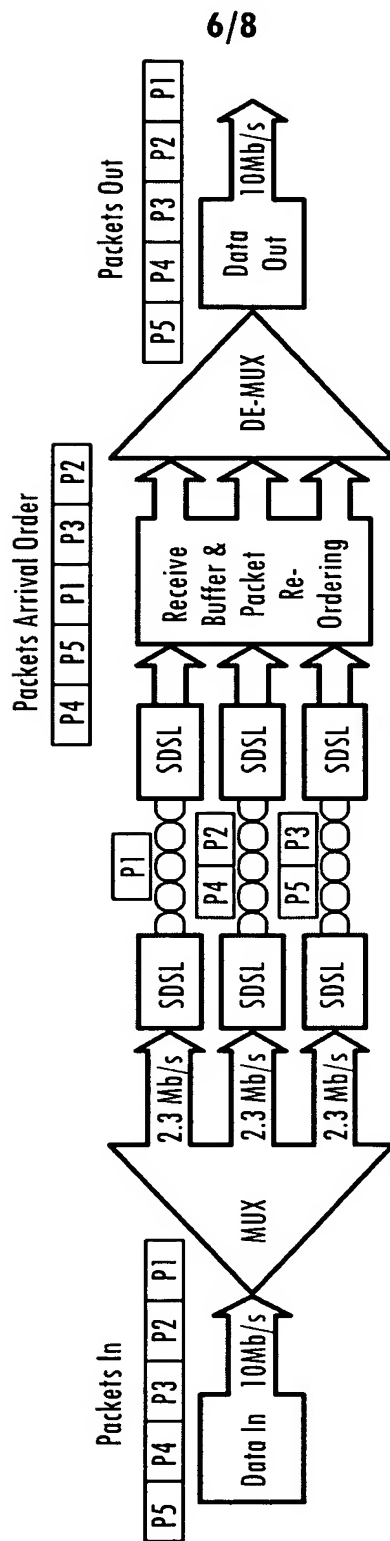


**Fig. 4**

5/8



**Fig. 5**



*Fig. 6*

7/8

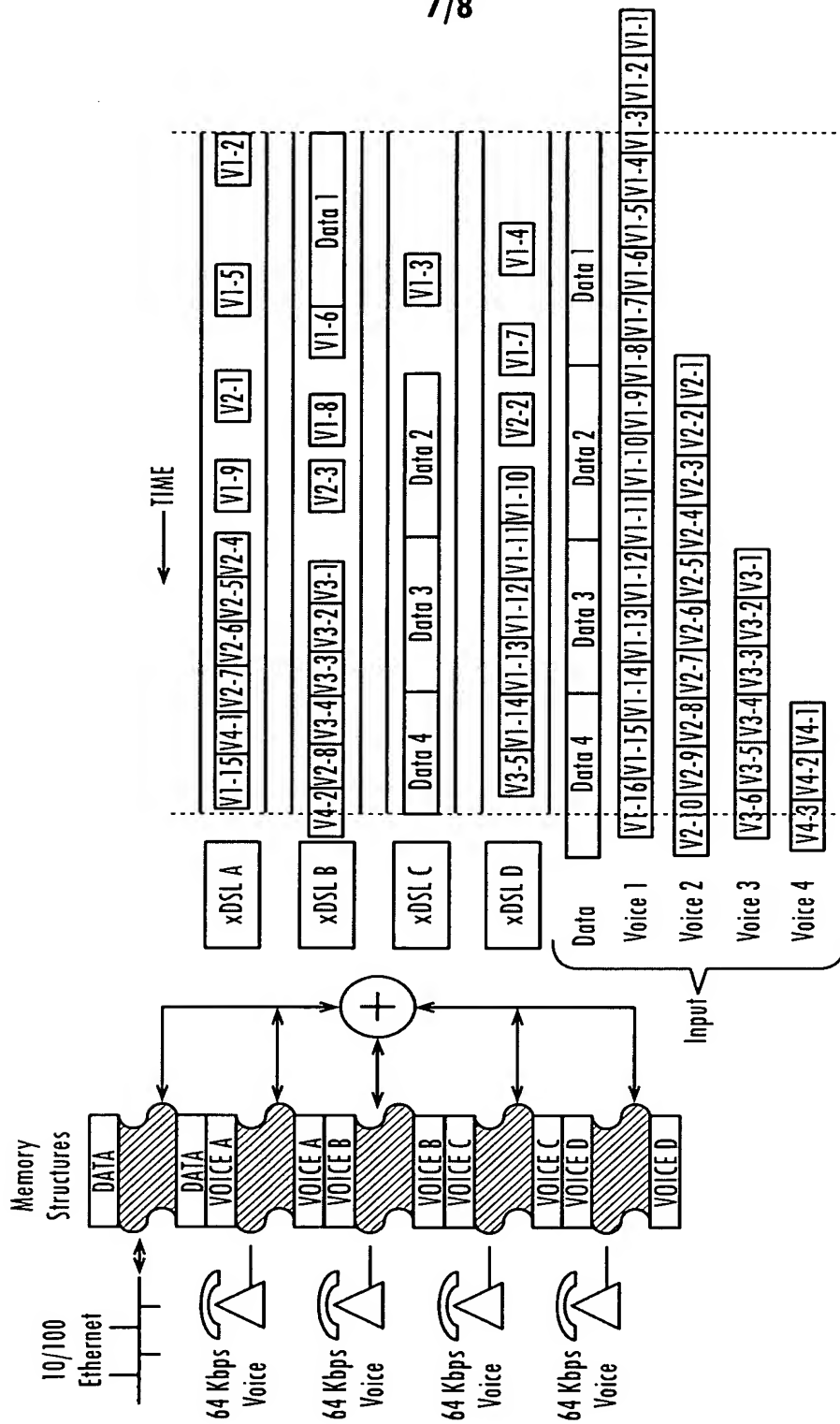
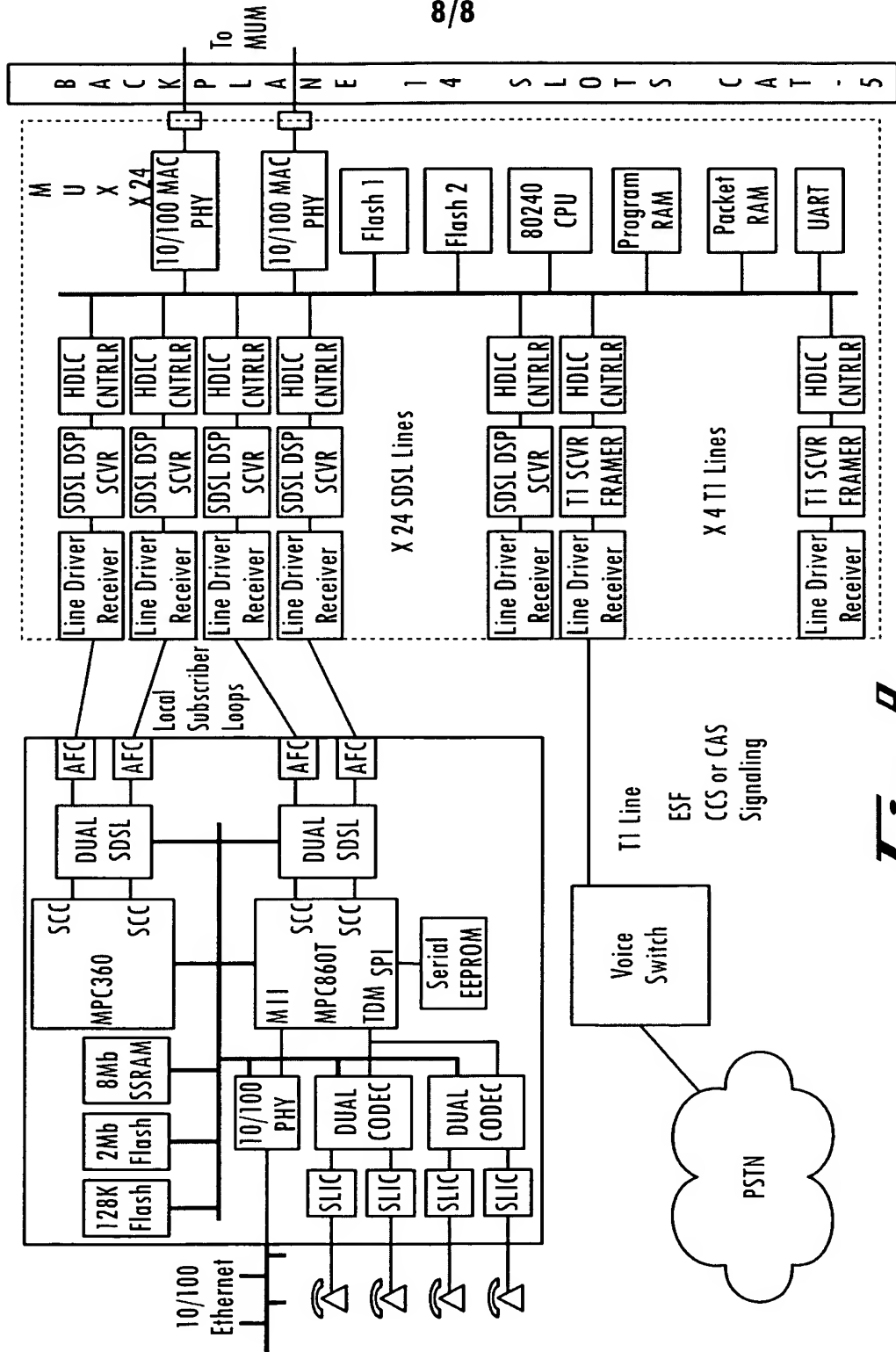


Fig. 7

8/8



**Fig. 8**